



FIG. 2

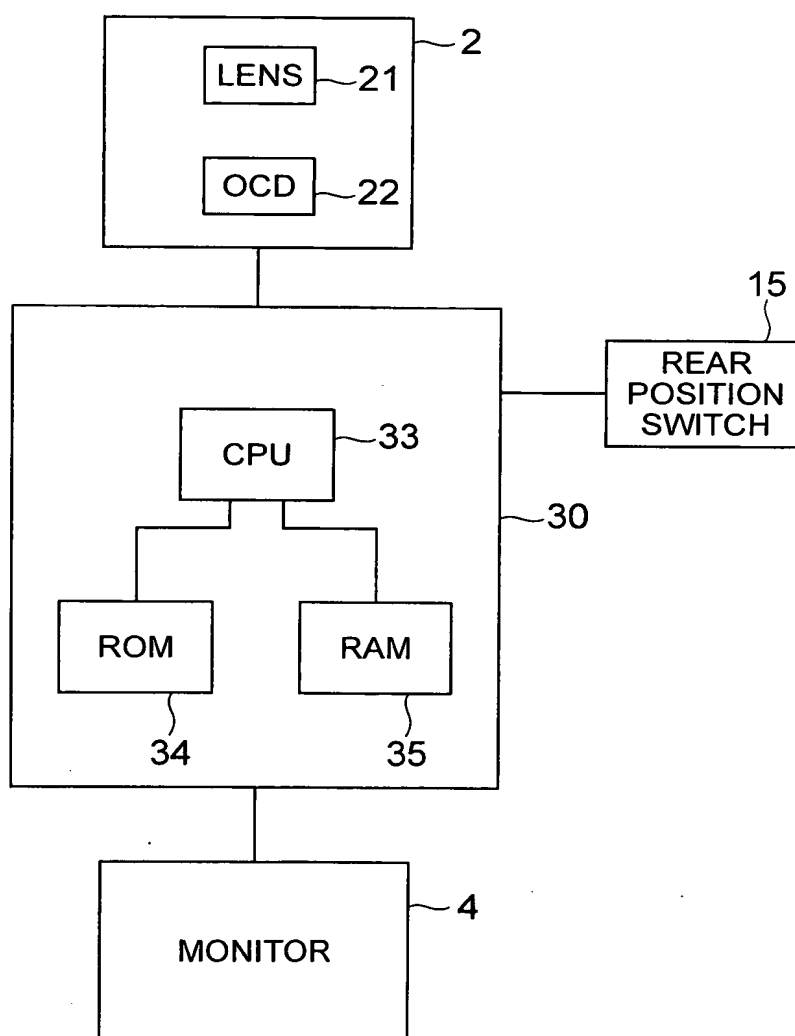


FIG. 3A

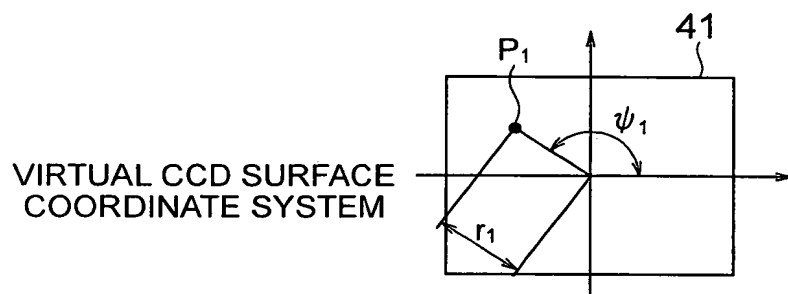


FIG. 3B

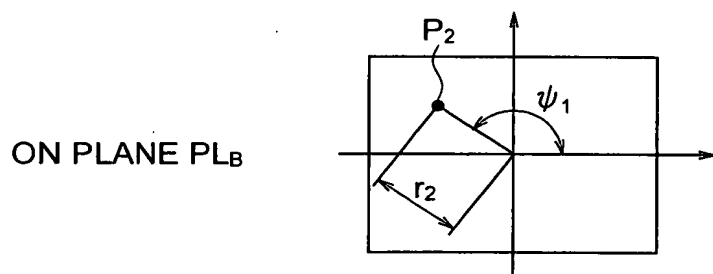


FIG. 3C

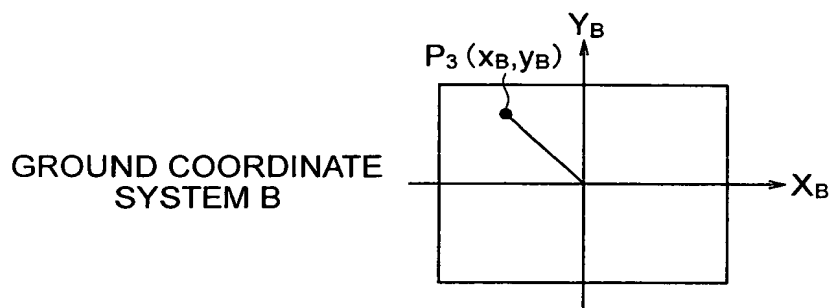


FIG. 4A

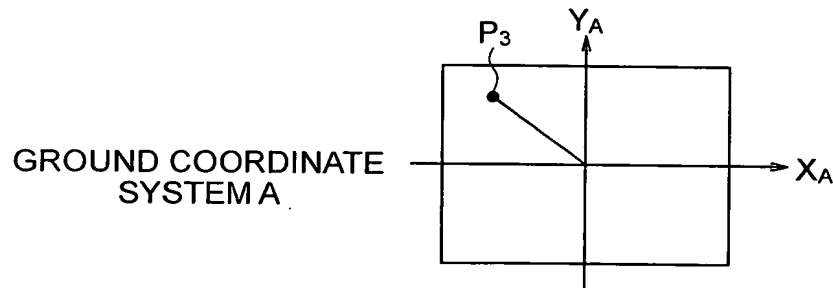


FIG. 4B

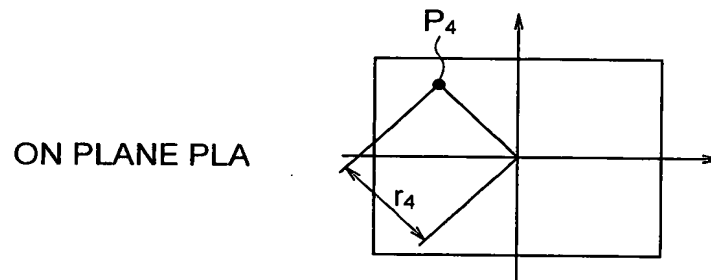


FIG. 4C

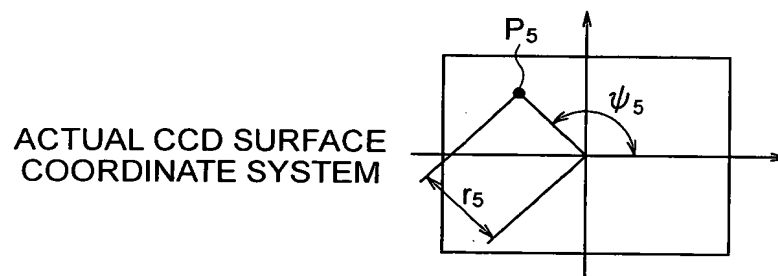


FIG. 4D

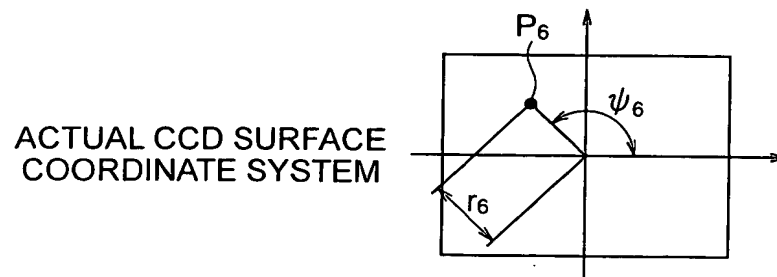


FIG. 5A

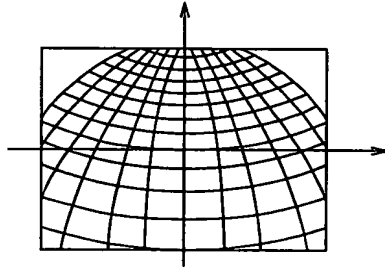


FIG. 5B

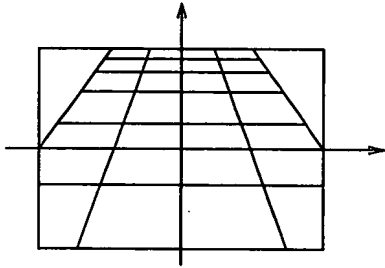


FIG. 5C

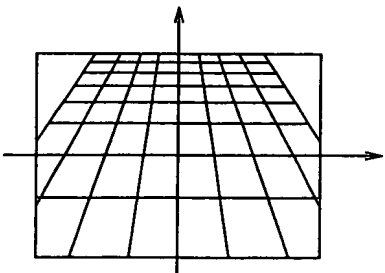


FIG. 6

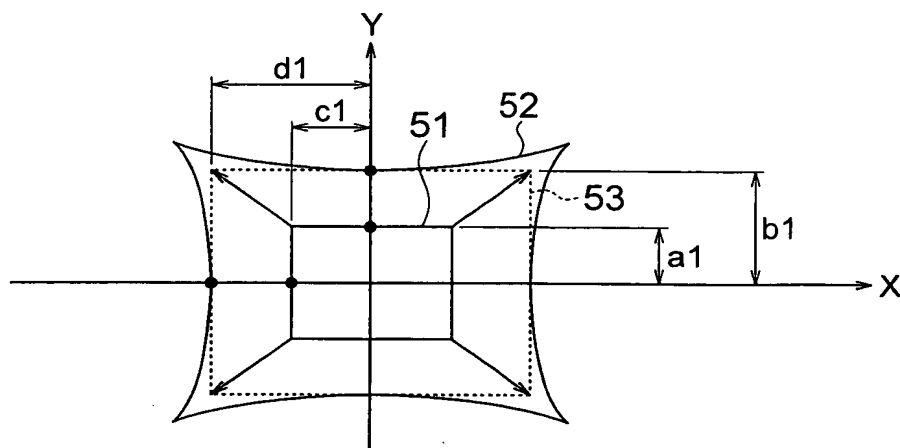


FIG. 7A

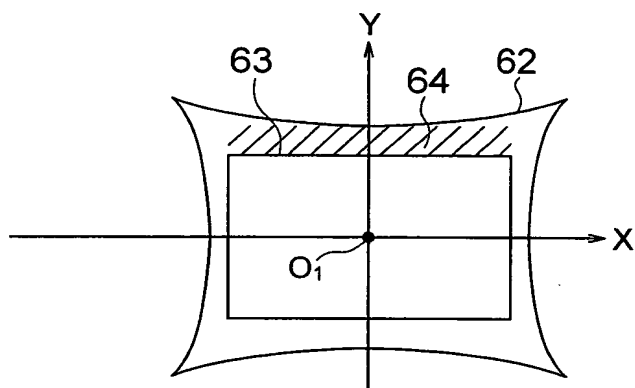


FIG. 7B

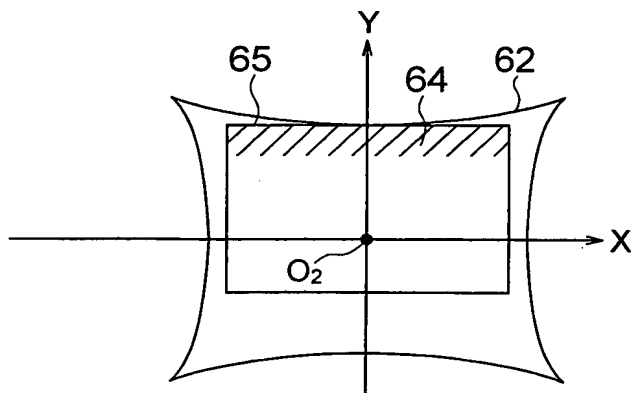
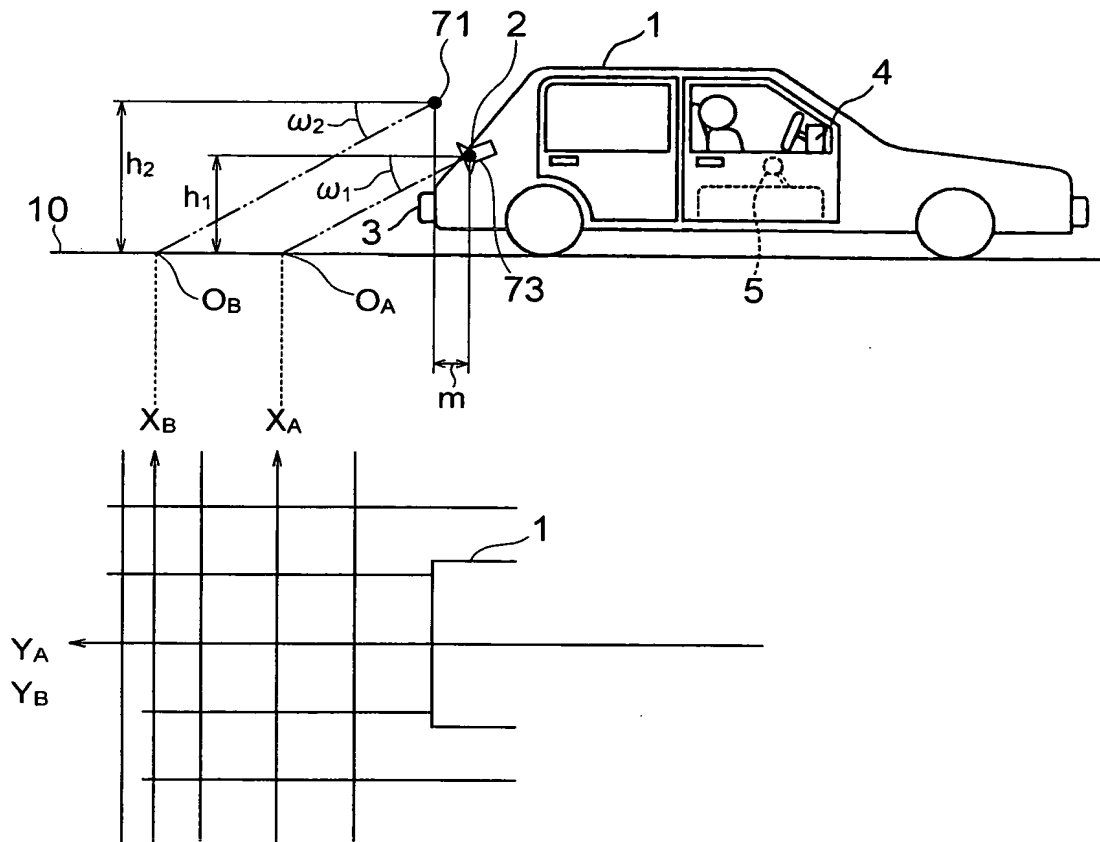


FIG. 8



The block diagram illustrates the system architecture. At the top, a box labeled 2 contains two sub-components: a LENS (21) and an OCD (22). A vertical line connects box 2 to a larger central box labeled 30. Inside box 30, a CPU (33) is connected to ROM (36) and RAM (35) via a horizontal bus. To the right of box 30, a REAR POSITION SWITCH (15) and a STEERING ANGLE SENSOR (81) are connected to the system bus. At the bottom, a MONITOR (4) is connected to the central box 30 via a vertical line.



FIG. 10A

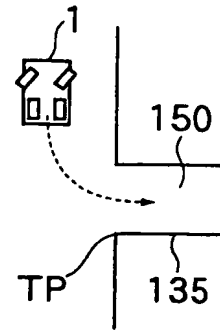
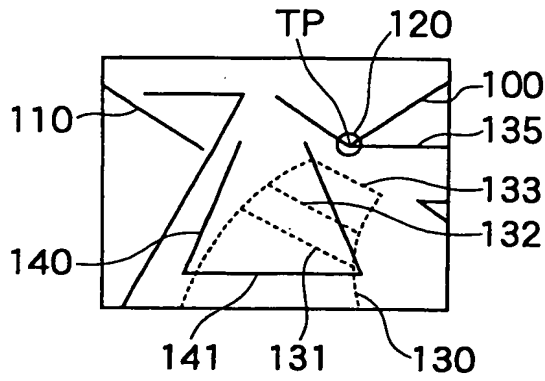


FIG. 10B

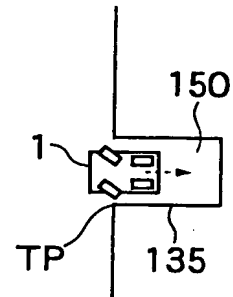
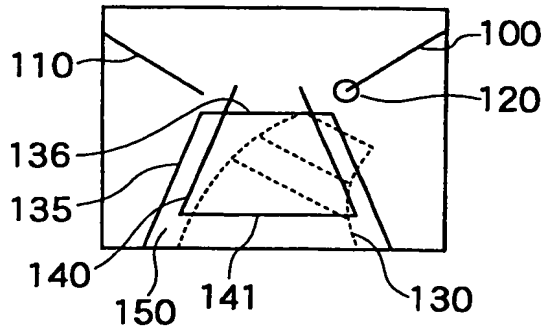


FIG. 10C

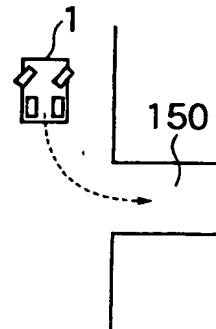
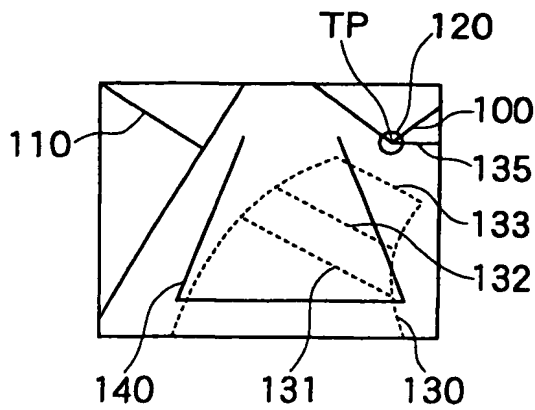


FIG. 11

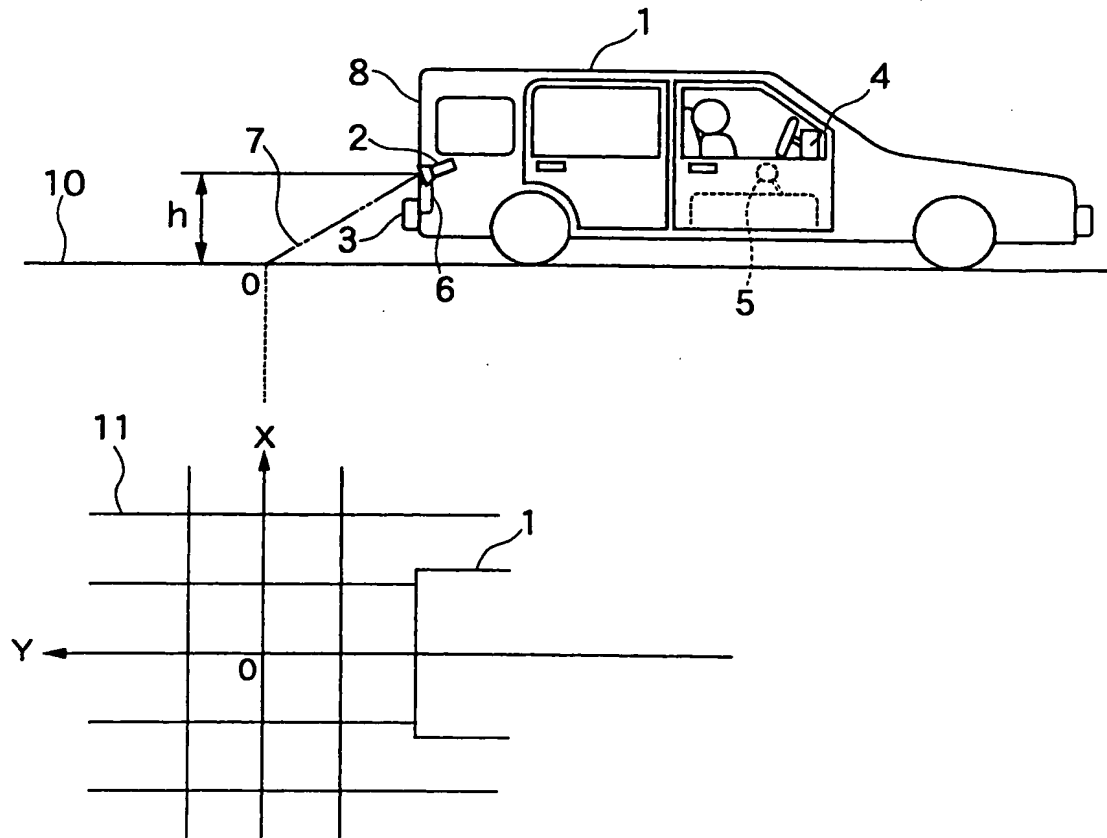


FIG. 12

